

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

DOUGLAS BRISBIN, et al.

Application No.: NEW

Filed: Herewith

**For: PMOS DEVICE WITH DRAIN  
JUNCTION BREAKDOWN POINT  
LOCATED FOR REDUCED DRAIN  
BREAKDOWN VOLTAGE WALK-IN  
AND METHOD FOR DESIGNING AND  
MANUFACTURING SUCH DEVICE**

Group Art Unit: Unknown

Examiner: Unknown

**INFORMATION  
DISCLOSURE  
STATEMENT**

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San Francisco, CA 94104  
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Mail Stop PATENT APPLICATION  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants submit herewith patents, publications or other information [attached hereto and listed on the attached Form PTO-1449 (modified)] of which they are aware, which they believe may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

This Information Disclosure Statement:

- (a) ☒ accompanies the new patent application submitted herewith. 37 CFR § 1.97(a).
- (b) ☐ is filed within three months after the filing date of the application or within three months after the date of entry of the national stage of a PCT application as set forth in 37 CFR § 1.491.
- (c) ☐ as far as is known to the undersigned, is filed before the mailing date of a first Office Action on the merits.

- (d) ☐ is filed after the first office action and more than three months after the application's filing date or PCT national stage date of entry filing but, as far as is known to the undersigned, prior to the mailing date of either a final rejection or a notice of allowance, whichever occurs first, and is accompanied by either the fee (\$180) set forth in 37 CFR § 1.17(p) or a certification as specified in 37 CFR § 1.97(e), as checked below.
- (e) ☐ is filed after the mailing date of either a final rejection or a notice of allowance, whichever occurred first, and the Issue Fee has not been paid, and is accompanied by the fee (\$130) set forth in 37 CFR § 1.17(i)(1) and a certification as specified in 37 CFR § 1.97(e), as checked below. This document is to be considered as a petition requesting consideration of the information disclosure statement.

[If either of boxes (d) or (e) are checked above, the following "certification" under 37 CFR § 1.97(e) may need to be completed.] The undersigned certifies that:

- (f) ☐ Each item of information contained in the information disclosure statement was cited in a communication mailed from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.
- (g) ☐ No item of information contained in this information disclosure statement was cited in a communication mailed from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of this information disclosure statement.

A list of the patent(s) or publication(s) is set forth on the attached Form PTO-1449 (Modified).

A copy of each of the items on PTO-1449 (Modified) is supplied herewith.

A concise explanation of relevance of the items listed on form PTO-1449 (Modified) is as follows. The attached paper by inventor Douglas Brisbin, inventor Andrew Strachan, and Prasad Chaparala, entitled "Breakdown Walk-in: A New PMOS Failure Mode in High Power BiCMOS Applications," was presented orally by Douglas Brisbin at the

IEEE 2003 International Integrated Reliability Workshop, on October 22, 2003, and was published (in 2004) in the 2003 IEEE International Integrated Reliability Workshop Final Report. The paper discloses aspects of the invention. The undersigned has been advised by inventor Douglas Brisbin that although Mr. Chaparala contributed to preparation of the paper, Mr. Chaparala did not conceive any aspect of the invention that is disclosed in the paper.

While the information and references disclosed in this Information Disclosure Statement may be "material" pursuant to 37 CFR § 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR § 1.56(a) exists. It is submitted that the Information Disclosure Statement is in compliance with 37 CFR § 1.98 and MPEP § 609 and the Examiner is respectfully requested to consider the listed references.

□ The Commissioner is hereby authorized to charge our Deposit Account No. **50-1697** for any fees required in connection with the filing of this Information Disclosure Statement. **A duplicate copy of this Notice is enclosed for this purpose.**

Respectfully submitted,

Dated: 4/16/04

By Alfred A. Equitz  
Alfred A. Equitz  
Registration No. 30,922  
Tel. No. 415/433-2250

Our File: NSC1-H1400 (P05941)

FORM PTO-1449 (Modified) U.S Dept. of Commerce (Rev. 7-80)                      Patent and Trademark Office  <b>INFORMATION DISCLOSURE CITATION</b>  (Use several sheets if necessary)				Atty Docket No.  NSC1-H1400 (P05941)		Appln. No.  NEW	
				Applicant(s) DOUGLAS BRISBIN, et al.			
				Filing Date Herewith		Group Unknown	
<b>U.S. PATENT DOCUMENTS</b>							
*Examiner Initials		Document Number	Date	Name	Class	Subclass	Filing Date
	AA						
	AB						
	AC						
<b>FOREIGN PATENT DOCUMENTS</b>							
*Examiner Initials		Document Number	Date	Country	Class	Subclass	Translation YES    NO
	AD						
	AE						
	AF						
<b>OTHER DOCUMENTS</b>							
	AG	D. Brisbin, A. Strachan, and P. Chaparala, " <i>Breakdown Walk-in: A New PMOS Failure Mode in High Power BiCMOS Applications</i> ," presented at the IEEE 2003 International Integrated Reliability Workshop, at S. Lake Tahoe, California, on October 22, 2003, and published in the 2003 IEEE International Integrated Reliability Workshop Final Report (February 2004) (four pages)					
	AH						
	AI						
Examiner					Date Considered		
* Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							